

☐ L1: (2435) command near3 argument
☐ L2: (5) burst near9 1
☐ L3: (65) order near9 1
☐ L4: (64) 3 not 2
☐ L6: (1) 1 with 5
☐ L7: (1) burst near9 5
☐ L5: (554) display near5 codec
☐ L8: (82) display with 1
☐ L9: (30) count with 1
☐ L10: (0) command near9 burst near9 reverse near9 order
☐ L11: (329) command near9 reverse near9 order
☐ L12: (172) 2 3 4 6 7 8 9
☒ L13: (326) 11 not 12

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20060008248 A1	20060112	17	Optimal buffering and scheduling strategy for smooth reverse in a DVD	386/68	386/125		Agranarc
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050276059 A1	20051215		Rear-view mirror with multifunctional signal	362/494			Rodrigue, ai.
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050243569 A1	20051103		Rear view side mirror with signals emitter device	362/494			Rodrigue, ai.
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050243568 A1	20051103		Set of parts and assembling method for assembling a rear-view side mirror	362/494			Rodrigue, ai.
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20050227810 A1	20051013	12	Drive device for working vehicle and method of controlling the drive of	477/172			Nakagawa
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050225994 A1	20051013		Rear-view mirror with a reflected combined signal and optics	362/494			Rodrigue, ai.
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20050206585 A1	20050922		Display devices and integrated circuits	345/34			Stewart, i
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20050165862 A1	20050728	9	Autonomic and fully recovering	707/202			Loafman, i

IS&R:

Pending

Active

Failed

Saved

Favorites

Tagged (7)

UDC

Queue

Trash

L1: (147) {("4888731") or ("4890260") or ("4928260") or ("6029212") or ("5940620

L2: (3660) burst near? with? read?

L3: (2965) command near? argument

L4: (1) 2 with 3

L5: (965) address? with 2

L6: (80976) (start? begin?) near? address?

L7: (182) 5 with 6

L8: (147) 1 not 7

L9: (5) burst near? command near? argument

L10: (2599) burst near? count

L11: (2) 3 with 10

L13: (329) 7 8

L14: (74) 12 not 13

L12: (80) command near? 10

USPGPU, USPAT, USOCR, EPO, IPO, DERWENT, EMI, TDB

Default operator: OR

command near? 10

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020056061 A1	20020509	37	Circuit for reducing test time and semiconductor memory device	714/718	714/736; 714/817		Haraguchi
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6779139 B2	20040817	36	Circuit for reducing test time and semiconductor memory device	714/718	714/736		Haraguchi
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6606701 B1	20030812	31	Micro-processor	712/207	710/52; 710/54;		Isibota; f
4	<input type="checkbox"/>	<input type="checkbox"/>	US 4366539 A	19821228	38	Memory controller with burst mode capability	711/218			Johnson;
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5835965 A	19981110	23	Memory system with multiplexed input-output port and memory	711/211	711/202		Taylor, Ro
6	<input type="checkbox"/>	<input type="checkbox"/>	US 4370712 A	19830125	40	Memory controller with address independent burst mode capability	711/201	365/230.01		Johnson;
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6742098 B1	20040525	18	Dual-port buffer-to-memory interface	711/172	365/230.05; 710/307;		Haibert; J
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6427197 B1	20020730	50	Semiconductor memory device	711/169	365/194;		Sato; Yasu

Ready



Drafts

- IS&R:
- Pending
- Active
- Failed
- Saved

- S2: (147) (["4888731"] or ["4890260"] or ["4928260"] or ["6029212"] or ["5940620"]
- S3: (3660) burst near? writ\$4 near? read\$4
- S4: (2965) command near? argument
- S5: (1) S3 with S4
- S6: (965) address\$4 with S3
- S7: (80976) (start\$4 begin\$4) near? address\$4
- S8: (182) S6 with S7
- S9: (147) S2 not S8
- S10: (5) burst near? command near? argument
- S11: (2599) burst near? count
- S12: (2) S4 with S11
- S14: (329) S8 S9
- S15: (74) S13 not S14
- S13: (80) command near? S11

Favorites

- logged (?)
- UDC
- Queue
- Trash

USPG PUB, USPAT, USOCR, EPO, JPO, DERWENT, BM, TDB

Default generator: OR

Print

☐ (highlight all to terms initially)

DRG form ISRT form Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR v	Current XRef	Retrieval Class	It
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030172240 A1	20030711	13	Method and apparatus for clock synchronization between a system	711/167	711/110		Johnson, Brice
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5829016 A	19981027	11	Memory system with multiplexed input-output port and systems and	711/111	365/189.02; 365/230.02		Sharma; Sud
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6665749 B1	20031216	19	Bus protocol for efficiently transferring vector data	710/29	710/307; 710/33;		Ansari; Ahme
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5644784 A	19970701	13	Linear list based DMA control structure	710/24	710/35		Peek; Gregg
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5765022 A	19980609	12	System for transferring data from a source device to a target device in	710/22	710/26; 710/33;		Kaiser, John
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6229759 B1	20010508	16	Semiconductor memory burst length count determination method	365/236	365/189.05; 365/233;		Morgan; Do
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5347634 A	19940913	12	System and method for directly executing user DMA instruction from	345/539	222/500		Herrell; Russ